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Application Number	10/675,872
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Vo, Ted T.
Art Unit	2192
Attemos Decket No.	AUC020030497UC1

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Name of Patentee or

Applicant of Cited Document

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U.S. Patent and Trademark Office. U.S. DEPARTMENT OF COMMERCE and the Patrwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Complete if Known Substitute for form 1449/PTO 10/675,872 **Application Number** 09/30/2003 Filing Date First Named Inventor Levine et al. **INFORMATION** Vo, Ted T. **Examiner Name STATEMENT** 2192 Art Unit AUS920030487US1 Attorney Docket No. **Publication Date** Name of Patentee or Pages, Columns, Lines, Where Examiner **Document Number** MM-DD-YYYY **Applicant of Cited Document** Initiats Relevant Passages or Relevant Figures Appear AA 5,822,578 Oct. 13, 1998 Frank et al. Th AB 6,574,727 B1 Jun. 3, 2003 Davidson et al. TTU **FOREIGN PATENT DOCUMENTS** Pages, Columns, Lines, Where **Publication Date** Examiner Name of Patentee or **Foreign Patent Document** Relevant Passages or Relevant MM-DD-YYYY Initial **Applicant of Cited Document** Figures Appear NON PATENT LITERATURE Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item Examiner Initial (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, 币

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Application Number 10/675,872

Filing Date 09/30/2003

First Named Inventor Levine et al.

Examiner Name Vo, Ted T.

Art Unit 2192

Attorney Docket No. AUS920030487US1

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